

An input signal includes spread signals which are respectively transmitted over a plurality of channels. First and second despread demodulators demodulate the spread signals transmitted over first and second communications channels by despreading them with first and second spread codes, respectively. A path detector generates a timing signal for instructing the timing at which the despread demodulators perform the despread operation. The path detector is shared by the first and the second despread demodulators, and operates in a time-division manner.